

Docket No.: 236518US41YA

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION: Eric J. STRANG, et al.

SERIAL NO.: 10/673,138

GAU: 2128

FILED: September 30, 2003

EXAMINER: A. Saxena

FOR: SYSTEM AND METHOD FOR USING FIRST-PRINCIPLES SIMULATION TO
FACILITATE A SEMICONDUCTOR MANUFACTURING PROCESS

LETTER SUBMITTING REPLACEMENT DRAWING SHEET(S)

COMMISSIONER FOR PATENTS
Alexandria, VA 22313

SIR:

The following drawing sheets are submitted herewith:

1 Replacement Drawing Sheet _____ New Drawing Sheets
 _____ Annotated Drawing Sheets _____ Color Photographs/Drawing Sheets

Official Action dated _____
 Notice of Allowance/Issue Fee dated _____
 Other dated _____

The changes and/or modifications made include the following:

DEVICE MANUFACTURING LAB has been revised to read: DEVICE MANUFACTURING
FAB

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, L.L.P.



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